

**Amendments to the Claims:**

The listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

Please amend claims 1, 4, 6, 8, 9, 13 and 14 as follows:

1. (currently amended) A circuit, comprising:  
  
a first current limiting circuit including a first switch and a first current source coupled between a selector terminal and a first voltage bus, the first current source having a first fixed current limit value, the first current limiting circuit adapted to limit a current out of the selector terminal to the first fixed current limit value in response to a voltage on the selector terminal; and  
  
a second current limiting circuit including a second switch and a second current source coupled between the selector terminal and a second voltage bus, the second current source having a second fixed current limit value, the second current limiting circuit adapted to limit a current into the selector terminal to the second fixed current limit value in response to the voltage on the selector terminal.
2. (original) The circuit of claim 1 further comprising a plurality of voltage comparators coupled to the selector terminal.
3. (original) The circuit of claim 2 further comprising decoder circuit coupled to the plurality of voltage comparators.

4. (currently amended) The circuit of claim 1 wherein the first current limiting circuit further includes ~~a first switch and~~ a first variable current source comprising the first current source and a third current source coupled between the first voltage bus the selector terminal.

5. (original) The circuit of claim 4 wherein the first switch is adapted to conduct when the voltage on the selector terminal is below a first threshold voltage, wherein the first switch is adapted not to conduct when the voltage on the selector terminal is above a second threshold voltage.

6. (currently amended) The circuit of claim 5 wherein the second current limiting circuit further includes ~~a second switch and~~ a second variable current source comprising the second current source and a fourth current source coupled between the selector terminal and the second voltage bus.

7. (original) The circuit of claim 6 wherein the second switch is adapted to conduct when the voltage on the selector terminal is above a third threshold voltage, wherein the second switch is adapted not to conduct when the voltage on the selector terminal is below a fourth threshold voltage.

8. (currently amended) The circuit of claim 7 wherein the first current limiting circuit is adapted to limit the current out of the selector terminal to ~~[[a]]~~the first fixed current limit value when the voltage on the selector terminal is below a fifth threshold voltage, wherein the first current limiting circuit is adapted to limit the current out of the selector

terminal to [[a]] the second fixed current limit value when the voltage on the selector terminal is above a sixth threshold voltage.

9. (currently amended) The circuit of claim 8 wherein the second current limiting circuit is adapted to limit the current limit into the selector terminal to a third fixed current limit when the voltage on the selector terminal is above a seventh threshold voltage, wherein the second current limiting circuit is adapted to limit the current into the selector terminal to a fourth fixed current limit when the voltage on the selector terminal is below an eighth threshold voltage.

10. (original) The circuit of claim 7 wherein the first threshold voltage and the second threshold voltage are less than the third threshold voltage and the fourth threshold voltage.

11. (original) The circuit of claim 8 wherein the fifth threshold voltage and the sixth threshold voltage are lower than the first threshold voltage and the second threshold voltage.

12. (original) The circuit of claim 9 wherein the seventh threshold voltage and the eighth threshold voltage are higher than the third threshold voltage and the fourth threshold voltage.

13. (currently amended) The circuit of claim 8 wherein the first fixed current limit is less than the second fixed current limit.

14. (currently amended) The circuit of claim 9 wherein the third fixed current limit is less than the fourth fixed current limit.

15. (original) The circuit of claim 1 wherein the circuit is included in an integrated circuit device.

16. (original) The circuit of claim 15 wherein the integrated circuit device is a controller in a switching power supply.